

Clean Version of All Pending Claims

1. (Amended) An electronic display comprising:

a display medium comprising at least one capsule containing a plurality of electrophoretic particles dispersed in a fluid medium;

a transistor having a data line, a gate electrode and a pixel electrode and comprising a layer of insulating material of the transistor situated between a first layer of conductive material of the transistor that forms the gate electrode and a second layer of conductive material of the transistor that forms the data line and pixel electrode, the transistor for applying an addressing voltage to the display medium via the pixel electrode; and

a storage capacitor comprising a layer of insulating material of the storage capacitor situated between a first layer of conductive material of the storage capacitor and a second layer of conductive material of the storage capacitor, said storage capacitor in electrical communication with the display medium addressed by the transistor for reducing a rate of voltage decay across the display medium.

2. (Canceled)

3. (Amended) The display of claim 1 wherein one of said layers of material of said transistor and a respective one of said layers of material of said storage capacitor are a same continuous layer of material.

4. The display of claim 1 wherein said transistor and said storage capacitor each further comprise a layer of semiconducting material situated between said respective first layers of conductive material and said respective second layers of conductive material.

5. (Amended) The display of claim 4 wherein one of said layers of material of said transistor and a respective one of said layers of material of said storage capacitor are a same continuous layer of material.

6. (Amended) The display of claim 4 wherein said layer of semiconducting material is unpatterned.

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7. (Amended) The display of claim 1 wherein the storage capacitor is in electrical communication with a second gate line different from a first gate line in electrical communication with the gate electrode for addressing the display medium.

8. The display of claim 1 wherein the storage capacitor is in electrical communication with a conductor.

9. (Amended) The display of claim 1 wherein the second layer of conductive material of the storage capacitor forms a storage capacitor pixel electrode, and the first layer of conductive material of the storage capacitor forms a storage capacitor gate electrode.

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10. (Amended) The display of claim 1 wherein the layer of insulating material of the storage capacitor is patterned.

11. (Amended) The display of claim 1 wherein the layer of insulating material of the storage capacitor is unpatterned.

12. (Amended) The display of claim 1 wherein the layer of insulating material of the storage capacitor and the layer of insulating material of the transistor are a same layer.

13. (Cancelled)

14. (Cancelled)

15. (Amended) The display of claim 1 wherein a capacitance of the storage capacitor is greater than a capacitance of a pixel comprising a portion of the display medium.

16. (Amended) The display of claim 1 wherein a voltage decay time across a pixel comprising a portion of the display medium is based on the product of R_p and $(C_p + C_s)$ where R_p is a resistance of the pixel, C_p is a capacitance of the pixel, and C_s is a capacitance of the storage capacitor.

17. (Cancelled)

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18. (Amended) An electronic display comprising:

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a display medium comprising at least one capsule containing a plurality of electrophoretic particles dispersed in a fluid medium; and

a storage capacitor comprising a layer of insulating material situated between a first layer of conductive material and a second layer of conductive material, said storage capacitor in electrical communication with a pixel comprising a portion of the display medium for reducing a rate of voltage decay across the pixel.

19. (Canceled)

20. (Canceled)